

Title (en)
ASYNCHRONOUS TRANSFER MODE (ATM) MULTIPLEXER DEVICE

Title (de)
ATM-MULTIPLEXEREINRICHTUNG

Title (fr)
DISPOSITIF DE MULTIPLEXAGE MTA

Publication
EP 1033017 A1 20000906 (DE)

Application
EP 98962274 A 19981116

Priority
• DE 9803371 W 19981116
• DE 19751560 A 19971120

Abstract (en)
[origin: DE19751560A1] The arrangement is connected on one side with a switching component (X15) over a high-frequency data bus (DB), and on the other side with at least one subscriber connection arrangement over respectively at least one subscriber data bus (TB). The multiplexer unit (MUX) contains a reception module (R) for transmission of data from a subscriber connection arrangement to the switching component, and transmitting module (T) for transmission of data from the switching component to a subscriber connection unit. The reception module and the transmitting module comprise individual memories (FIFO0,..., FIFO3) for each subscriber data bus (TB0,..., TB3). Data in the reception module are written with an individual connection arrangement through the respective subscriber, with a pre-settable clock rate (TBCLOCK), into an individual memory. The data are read with a clock rate (DBCLOCK), pre-settable through the switching component. Data in the transmitting module are written into an individual memory, with the clock rate determined by the switching component, and are read with the clock rate determined by the respective subscriber connection arrangement.

IPC 1-7
H04L 12/56

IPC 8 full level
H04J 3/24 (2006.01); **H04Q 11/04** (2006.01); **H04L 12/70** (2013.01)

CPC (source: EP)
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Citation (search report)
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