

Title (en)  
Method for synthesizing a clock signal and synthesizing device thereof

Title (de)  
Verfahren zur Herstellung eines Taktsignales und Vorrichtung dazu

Title (fr)  
Méthode pour la synthèse d'un signal d'horloge et dispositif de synthèse

Publication  
**EP 1039641 B1 20040526 (EN)**

Application  
**EP 00440068 A 20000310**

Priority  
IT TO990201 A 19990315

Abstract (en)  
[origin: EP1039641A1] A method for synthesizing a clock signal, said clock signal being locked to a reference clock signal, said method providing for using a third clock signal, operating at a higher frequency. The method provides the steps of: measuring the reference clock signal (CK\_REF) by means of the third clock signal (CK\_HIGH), operating at a higher frequency, obtaining a measured value (MES) of the reference clock signal (CK\_REF) frequency; comparing the measured value (MES) with a nominal value; obtaining a correction value (CRR) as a function of the measured value (MES) and storing said correction value (CRR); using said correction value (CRR) for driving a digital controlled oscillator (OC) that outputs the synthesized clock signal (CK\_SYN). <IMAGE>

IPC 1-7  
**H03L 7/18**; **H04L 7/00**

IPC 8 full level  
**H03L 7/00** (2006.01); **H03L 7/02** (2006.01)

CPC (source: EP US)  
**H03L 7/00** (2013.01 - EP US); **H03L 7/02** (2013.01 - EP US)

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Cited by  
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