

Title (en)

CIRCUIT FOR REDUCING INPUT VOLTAGE

Title (de)

SCHALTUNGSANORDNUNG ZUR REDUZIERUNG EINER EINGANGSSPANNUNG

Title (fr)

CIRCUIT POUR REDUIRE UNE TENSION D'ENTREE

Publication

EP 1040400 A1 20001004 (DE)

Application

EP 99923390 A 19990324

Priority

- DE 9900868 W 19990324
- DE 19833092 A 19980723

Abstract (en)

[origin: DE19833092A1] The circuit has at least one potential divider (R1,R3,T1,R4;R7,RB,R2) with which the input voltage can be reduced to obtain the working voltage (Uz, Uarb) for an evaluation device. The potential divider(s) can be regulated so that the division factor can be increased with increasing input voltage and reduced with decreasing input voltage. An Independent claim is also included for a method of reducing an input voltage.

IPC 1-7

G05F 1/613; **H03G 1/00**

IPC 8 full level

G01B 7/00 (2006.01); **G01B 7/30** (2006.01); **G01D 5/244** (2006.01); **G01D 5/245** (2006.01); **G01P 3/48** (2006.01); **G01P 3/481** (2006.01); **G05F 1/44** (2006.01); **H03K 5/08** (2006.01)

CPC (source: EP KR US)

G01P 3/4802 (2013.01 - EP US); **G05F 1/613** (2013.01 - KR); **H03K 5/08** (2013.01 - EP US)

Citation (search report)

See references of WO 0005635A1

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

DE 19833092 A1 20000127; EP 1040400 A1 20001004; JP 2002521673 A 20020716; KR 20010030574 A 20010416; US 6275015 B1 20010814; WO 0005635 A1 20000203

DOCDB simple family (application)

DE 19833092 A 19980723; DE 9900868 W 19990324; EP 99923390 A 19990324; JP 2000561544 A 19990324; KR 20007002044 A 20000228; US 50845000 A 20000308