

Title (en)

A METHOD AND A DEVICE FOR DELAY REDUCTION IN A COMMUNICATION NETWORK

Title (de)

VERFAHREN UND EINRICHTUNG ZUR VERZÖGERUNGSREDUZIERUNG IN EINEM KOMMUNIKATIONSNETZ

Title (fr)

PROCEDE ET DISPOSITIF PERMETTANT DE REDUIRE LE DELAI DANS UN RESEAU DE COMMUNICATION

Publication

EP 1040709 A1 20001004 (EN)

Application

EP 98962772 A 19981204

Priority

- SE 9802219 W 19981204
- SE 9704766 A 19971219

Abstract (en)

[origin: WO9935878A1] A method and a device for reduction of the total delay of ATM traffic in a communication network where a multitude of nodes (2-4) share a common time multiplex physical medium (5) for transmitting ATM cells in an uplink direction (6). The nodes (2-4) comprise a circuit emulator (11) where the ATM cells which are to be transmitted are packed and a network terminal (8) comprising at least one PLL (27), a phase detector (26) and preferably a phase delay unit (24). The nominal frequency for the allocated time slots on the physical medium (5) is the same as the frequency for generation of the ATM cells. By using the above components the generation of ATM cells will be locked in frequency and phase to the allocated time slots in order to minimize the waiting time in the buffer (26) of the node.

IPC 1-7

H04Q 11/04; H04L 12/56

IPC 8 full level

H04Q 11/04 (2006.01); H04L 12/70 (2013.01)

CPC (source: EP)

H04Q 11/0478 (2013.01); H04L 2012/5649 (2013.01); H04L 2012/5674 (2013.01)

Citation (search report)

See references of WO 9935878A1

Designated contracting state (EPC)

DE ES FR IT

DOCDB simple family (publication)

WO 9935878 A1 19990715; AU 1793199 A 19990726; CA 2314908 A1 19990715; CN 1282501 A 20010131; EP 1040709 A1 20001004; SE 511594 C2 19991025; SE 9704766 D0 19971219; SE 9704766 L 19990620

DOCDB simple family (application)

SE 9802219 W 19981204; AU 1793199 A 19981204; CA 2314908 A 19981204; CN 98812363 A 19981204; EP 98962772 A 19981204; SE 9704766 A 19971219