

Title (en)

Method and circuit arrangement for quality information determination to detect errors in the transmission of a speech signal

Title (de)

Verfahren und Schaltungsanordnung zur Ermittlung einer Qualitätsinformation für die Fehlerentdeckung in der Übertragung eines Sprachsignals

Title (fr)

Méthode et circuit pour la détermination d'une information de qualité permettant la détection d'erreurs de transmission d'un signal de parole

Publication

EP 1052621 A3 20031105 (DE)

Application

EP 00440136 A 20000509

Priority

DE 19921504 A 19990510

Abstract (en)

[origin: EP1052621A2] The method involves estimating a first bit error rate from a first number of input bits and a second bit error rate from a second number of input bits. A transformation unit (4) processes and combines the two bit error rates according to a transformation rule to determine quality information for input to the fault detection unit (2) to improve speech quality. An Independent claim is also included for a circuit for implementing the method.

IPC 1-7

G10L 19/00; H04L 1/00

IPC 8 full level

G10L 19/00 (2006.01); **G10L 19/005** (2013.01)

CPC (source: EP)

G10L 19/005 (2013.01)

Citation (search report)

- [X] EP 0689312 A2 19951227 - NEC CORP [JP]
- [X] US 5432778 A 19950711 - MINDE TOR B [SE], et al
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- [X] EP 0805572 A2 19971105 - TEXAS INSTRUMENTS INC [US]

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WO2004045132A1; US7035347B2

Designated contracting state (EPC)

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)

EP 1052621 A2 20001115; **EP 1052621 A3 20031105**; AU 2777500 A 20001116; DE 19921504 A1 20001123

DOCDB simple family (application)

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