

Title (en)
Interconnection between a computer and a display

Title (de)
Verbindung zwischen Computer und Anzeigegerät

Title (fr)
Interconnexion entre ordinateur et dispositif d'affichage

Publication
EP 1058228 A2 20001206 (EN)

Application
EP 00112041 A 20000602

Priority
JP 15583699 A 19990603

Abstract (en)
A display device includes video-signal-processor for receiving an input-video-signal and pixel-converting-circuit for converting a pixel of an output signal from the video signal processor so that display device can display the input video signal as an appropriate video on a pixel-displaying device, where the display device receives pixel-format-data of the input video signal through a control bus independent of the input video signal. A computer system includes a computer having video-signal-outputting-section for outputting a video signal to display device, video-signal-data-outputting-section for outputting format data of the video signal output from section, and a control bus for transmitting the format data of the input video signal as well as a request of transferring the format data from the display device. <IMAGE>

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G09G 3/20; **G06F 3/147**

IPC 8 full level
G06F 3/153 (2006.01); **G09G 3/20** (2006.01); **G09G 5/00** (2006.01); **H04N 5/66** (2006.01)

CPC (source: EP KR)
G09G 5/00 (2013.01 - KR); **G09G 5/006** (2013.01 - EP); **G09G 5/005** (2013.01 - EP); **G09G 2370/045** (2013.01 - EP)

Citation (examination)
US 5748167 A 19980505 - WATANABE GAKU [JP], et al

Cited by
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Designated contracting state (EPC)
DE FR GB

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EP 1058228 A2 20001206; **EP 1058228 A3 20050727**; CN 1276589 A 20001213; JP 2000347637 A 20001215; KR 100386773 B1 20030609; KR 20010014994 A 20010226; MY 122646 A 20060429; TW 469738 B 20011221

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