

Title (en)

Method and apparatus for processing video signals for display

Title (de)

Verfahren und Anordnung zur Verarbeitung von Videosignalen

Title (fr)

Procédé et dispositif pour traitement des signaux vidéo pour affichage

Publication

EP 1058229 A1 20001206 (EN)

Application

EP 00107963 A 20000417

Priority

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- EP 99401036 A 19990428

Abstract (en)

With the new plasma display panel technology new kinds of artefacts can occur in video pictures due to the principle that brightness control is done with a modulation of small lighting pulses in a number of periods called sub-fields. These artefacts are commonly described as 'dynamic false contour effect'. A technique called bit line repeat coding has been developed for reducing the false contour effect. According to this technique sub-field coding is done with common (CSF) and normal sub-fields (SF) where for the common sub-fields (CSF) identical entries in the sub-field code words of two or more corresponding pixels on two or more pixel lines are used. In this specific sub-field coding method some cases will occur in which an error has to be made due to the reduced flexibility in encoding produced by the need to have the same code on common sub-fields (CSF). The general idea of the invention is now to put the coding failures on the higher video levels of the two or more pixels being grouped together. Further improvements concern picture content analysis and or motion detection for controlling the switching between different sub-field coding modes and a specific adapted dithering pattern for use with bit line repeat coding. <IMAGE>

IPC 1-7

G09G 3/28

IPC 8 full level

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CPC (source: EP)

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G09G 2320/0266 (2013.01); **G09G 2320/10** (2013.01); **G09G 2320/106** (2013.01)

Citation (search report)

- [AD] EP 0874349 A1 19981028 - THOMSON MULTIMEDIA SA [FR]
- [A] PATENT ABSTRACTS OF JAPAN vol. 199, no. 711 11 July 1999 (1999-07-11)

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