Title (en)

QUADRATURE RECEIVER, COMMUNICATION SYSTEM, SIGNAL PROCESSOR, METHOD OF CALCULATING DIRECT CURRENT OFFSET, AND METHOD OF OPERATING A QUADRATURE RECEIVER

Title (de)

QUADRATUREMPFÄNGER, KOMMUNIKATIONSSYSTEM, SIGNALPROZESSOR, VERFAHREN ZUM BERECHNEN DES GLEICHSTROM-OFFSETS, UND VERFAHREN ZUM BETRIEB EINES QUADRATUREMPFÄNGERS

Title (fr)

RECEPTEUR EN QUADRATURE, SYSTEME DE COMMUNICATION, PROCESSEUR DE SIGNAUX, CALCUL DU DECALAGE DE COURANT CONTINU, ET MODE DE FONCTIONNEMENT DUDIT RECEPTEUR

Publication

EP 1062781 A1 20001227 (EN)

Application

EP 99968953 A 19991222

Priority

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- US 21923598 A 19981222

Abstract (en)

[origin: WO0038385A1] A quadrature receiver (10) including a downconverter (22) configured to convert a wireless communication signal into a baseband signal; and an I/Q offset processor (40) coupled with the downconverter (22) and configured to sample the baseband signal to provide plural sampled vectors, to generate a difference vector from the sampled vectors, to generate a scaled vector from the difference vector, and to sum the difference vector, the scaled vector and one of the sampled vectors to provide a direct current offset signal. A method of calculating direct current offset including receiving a wireless communication signal; downconverting the wireless communication signal following the receiving; sampling the wireless communication signal providing plural sampled vectors; calculating a difference vector from the sampled vectors; calculating a scaled vector from the difference vector; and summing the difference vector, the scaled vector and one of the sampled vectors to provide a direct current offset signal.

IPC 1-7

H04L 25/06; H04L 27/152

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