

Title (en)

Crystal-axis-aligned vertical side wall DRAM and process for manufacture thereof

Title (de)

DRAM über einer zur kristallografischen Achse vertikal ausgerichteten Seitenwand und Verfahren zu dessen Herstellung

Title (fr)

DRAM sur une paroi latérale verticale alignée avec un axe cristallographique et son procédé de fabrication

Publication

**EP 1071129 A3 20010613 (EN)**

Application

**EP 00306232 A 20000721**

Priority

US 35929299 A 19990722

Abstract (en)

[origin: EP1071129A2] A dynamic random access memory (DRAM) cell comprising a deep trench storage capacitor having an active transistor device partially disposed on a side wall of the trench. The side wall is aligned to a first crystallographic plane having a crystallographic orientation along a single crystal axis. A process for manufacturing such a DRAM cell comprises: (a) forming a deep trench in a substrate, (b) forming a faceted crystal region along the trench side wall having a single crystallographic orientation, and (c) forming a transistor device partially disposed on the faceted crystal region in the side wall. The faceted crystal region may be formed by growing an oxide collar, such as by local thermal oxidation under oxidation conditions selected to promote a higher oxidation rate along a first family of crystallographic axes than along a second family of crystallographic axes. <IMAGE>

IPC 1-7

**H01L 21/8242**; **H01L 27/108**

IPC 8 full level

**H10B 12/00** (2023.01)

CPC (source: EP KR US)

**H10B 12/00** (2023.02 - KR); **H10B 12/0383** (2023.02 - EP US); **H10B 12/0387** (2023.02 - EP US); **H10B 12/053** (2023.02 - EP US)

Citation (search report)

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- [XAY] US 4971926 A 19901120 - KINUGAWA MASAOKI [JP]
- [A] US 5543348 A 19960806 - HAMMERL ERWIN [US], et al
- [A] PATENT ABSTRACTS OF JAPAN vol. 013, no. 277 (E - 778) 26 June 1989 (1989-06-26)
- [A] PATENT ABSTRACTS OF JAPAN vol. 012, no. 481 (E - 694) 15 December 1988 (1988-12-15)

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Designated contracting state (EPC)

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)

**EP 1071129 A2 20010124**; **EP 1071129 A3 20010613**; CN 1159766 C 20040728; CN 1301042 A 20010627; JP 2001044390 A 20010216; JP 3434488 B2 20030811; KR 100659810 B1 20061219; KR 20010029961 A 20010416; TW 461087 B 20011021; US 2001038113 A1 20011108; US 6320215 B1 20011120; US 6426251 B2 20020730

DOCDB simple family (application)

**EP 00306232 A 20000721**; CN 00120497 A 20000712; JP 2000209997 A 20000711; KR 20000041237 A 20000719; TW 89106878 A 20000413; US 35929299 A 19990722; US 89442701 A 20010628