Title (en)

METHOD AND APPARATUS FOR LOW JITTER CLOCK RECOVERY

Title (de)

VERFAHREN UND VORRICHTUNG ZUR RÜCKGEWINNUNG VON TAKT MIT GERINGEM ZITTERN

Title (fr)

PROCEDE ET APPAREIL POUR RECUPERATION A FAIBLE INSTABILITE DU SIGNAL DE SYNCHRONISATION

Publication

EP 1076846 A2 20010221 (EN)

Application

EP 99921837 A 19990511

Priority

- US 9910226 W 19990511
- US 8502198 P 19980511

Abstract (en)

[origin: WO9959060A2] A Method and system for distributing instructions for processing source data along with the source data to an extensible device. The extensible device receives the source data along with instructions for processing the source data. The extensible device in one embodiment includes a central processing unit, a memory, and output interface. The extensible device stores the instructions in memory as the instructions are received. The extensible device then directs the central processing unit to process the received source data in accordance with the instructions that have been stored in the memory. As part of this processing, the extensible device may send data to the output interface. In this way, the instructions for processing the source data can be customized to the source data or can include improvements in processing source data.

IPC 1-7

G06F 1/00

IPC 8 full level

G06F 9/445 (2006.01); **G06F 9/24** (2006.01); **G06F 9/318** (2006.01); **G06F 13/10** (2006.01); **G06F 13/38** (2006.01); **H04J 3/00** (2006.01); **H04J 3/06** (2006.01); **H04L 7/00** (2006.01); **H04L 7/033** (2006.01); **H04L 12/28** (2006.01); **H04L 12/40** (2006.01); **H04N 5/21** (2006.01); **H04N 7/24** (2011.01); H04L 12/64 (2006.01)

CPC (source: EP)

G06F 9/24 (2013.01); **G06F 9/30189** (2013.01); **H04J 3/0632** (2013.01); **H04L 12/40058** (2013.01); **H04L 12/40117** (2013.01); **H04N 21/43632** (2013.01)

Citation (search report)

See references of WO 9959047A2

Designated contracting state (EPC)

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)

WO 9959060 A2 19991118; **WO 9959060 A3 19991229**; AU 3792199 A 19991129; AU 3792299 A 19991129; AU 3894699 A 19991129; AU 3897099 A 19991129; CA 2330676 A1 19991118; CA 2330739 A1 19991118; CA 2330740 A1 19991118; CA 2330970 A1 19991118; EP 1076846 A2 20010221; EP 1076850 A2 20010221; EP 1076858 A2 20010221; EP 1101303 A2 20010523; JP 2002514810 A 20020521; JP 2002514820 A 20020521; JP 2002514876 A 20020521; JP 2002515718 A 20020528; WO 9959047 A2 19991118; WO 9959047 A3 20000406; WO 9959073 A2 19991118; WO 9959073 A3 19991229; WO 9959073 A9 20010531; WO 9959391 A2 19991118; WO 9959391 A3 20010322

DOCDB simple family (application)

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