

Title (en)

SAMPLER FOR A PICTURE DISPLAY DEVICE

Title (de)

ABTASTSCHALTUNG FÜR EINE BILDANZEIGEEINRICHTUNG

Title (fr)

ECHANTILLONNEUR CON U POUR UN AFFICHEUR D'IMAGES

Publication

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Application

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Abstract (en)

[origin: WO0052670A1] The invention relates to a sampler (2) and a method for converting a signal (S1) into a multiple signal (S220 ... S222), comprising a stage (22) of sample and hold circuits (220...222). The sampler (2) comprises an input circuit (20) having means (201) for applying a signal (S1) in bursts (S20) to the stage (22). Successive bursts are separated by a time interval (DELTA t1, DELTA t2). In this way, an increased sample time of the signal (S222) in the last channel is provided for a display panel (3) or a subsequent sample and hold circuit. In general, use of the invention ensures that the number of stages in a sampler can be reduced. The resulting, more compact design is very suitable for integration because the power consumption can be kept low. The arrangement requires less buffering, which makes it simpler to avoid uniformity problems and ghost images. A signal input in bursts requires memories. By using memories that are already present in the display device for scaling and frame buffering (21), no additional memories are required.

IPC 1-7

G09G 3/36; G09G 3/20

IPC 8 full level

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