

Title (en)

A BUS ARRANGEMENT FOR A DRIVER OF A MATRIX DISPLAY

Title (de)

BUSANORDNUNG FÜR ANSTEUERSCHALTUNG EINER MATRIXANZEIGEEINRICHTUNG

Title (fr)

SYSTEME DE BUS POUR PILOTE D'AFFICHEUR

Publication

**EP 1078352 A2 20010228 (EN)**

Application

**EP 99921838 A 19990511**

Priority

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- US 8576698 P 19980516

Abstract (en)

[origin: WO9960555A2] A demultiplexer applies picture information to pixels arranged in an array of a display device having columns and rows. The demultiplexer includes transistor switches each having a control terminal, an input terminal and an output terminal. A first buss couples switch control signals to the control terminals of the switches. The conductors of a first buss extend in a region containing each of the switches to form a global buss arrangement. Local busses have each conductors coupled to the input terminals of the switches associated with the individual local buss. The output terminals of the switches associated with the individual local buss are coupled to corresponding, consecutively disposed column conductors of the array. The individual local buss has a section that crosses over the first buss and a second section extending between the crossover section and the input terminals of the associated switches. The conductors of the second section extend in a region containing the associated switches and are absent from regions containing switches associated with the other local busses to obtain buss separation forming a local clustering buss arrangement.

IPC 1-7

**G09G 1/00**

IPC 8 full level

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CPC (source: EP KR)

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Citation (search report)

See references of WO 9960555A2

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