

Title (en)

ZERO DC CURRENT POWER-ON RESET CIRCUIT

Title (de)

EINSCHALT-RÜCKSETZSCHALTUNG OHNE GLEICHSTROMVERBRAUCH

Title (fr)

CIRCUIT DE REINITIALISATION A LA MISE SOUS TENSION A CONSOMMATION EN COURANT CONTINU NULLE

Publication

EP 1078465 A4 20010530 (EN)

Application

EP 99923014 A 19990513

Priority

- US 9910574 W 19990513
- US 8231498 A 19980520

Abstract (en)

[origin: WO9960703A1] The present invention provides a power-on reset circuit (300) that consumes zero DC current including a transistor (P1), capacitor (C), a resistor (R) and an inverter (302).

IPC 1-7

H03L 7/00; H03K 17/22

IPC 8 full level

H03K 17/22 (2006.01)

CPC (source: EP)

H03K 17/223 (2013.01)

Citation (search report)

- [X] US 4994689 A 19910219 - KIKUDA SHIGERU [JP], et al
- [X] PATENT ABSTRACTS OF JAPAN vol. 011, no. 061 (E - 483) 25 February 1987 (1987-02-25)
- [X] PATENT ABSTRACTS OF JAPAN vol. 006, no. 119 (E - 116) 3 July 1982 (1982-07-03)
- [X] PATENT ABSTRACTS OF JAPAN vol. 011, no. 165 (E - 510) 27 May 1987 (1987-05-27)
- [X] PATENT ABSTRACTS OF JAPAN vol. 013, no. 301 (E - 785) 11 July 1989 (1989-07-11)
- [PX] PATENT ABSTRACTS OF JAPAN vol. 1999, no. 05 31 May 1999 (1999-05-31)
- See references of WO 9960703A1

Designated contracting state (EPC)

DE FR GB IE IT

DOCDB simple family (publication)

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DOCDB simple family (application)

US 9910574 W 19990513; EP 99923014 A 19990513; JP 2000550210 A 19990513; TW 88106354 A 19990421