

Title (en)

VLSI EMULATOR COMPRISING PROCESSORS AND RECONFIGURABLE CIRCUITS

Title (de)

VLSI EMULATOR MIT PROZESSOREN UND REKONFIGURABLEN KREISLÄUFEN

Title (fr)

EMULATEUR A TRES HAUTE INTEGRATION COMPRENANT DES PROCESSEURS ET DES CIRCUITS RECONFIGURABLES

Publication

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Application

EP 00911468 A 20000317

Priority

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- KR 19990009307 A 19990319

Abstract (en)

[origin: WO0057273A1] Disclosed is an apparatus for verifying a VLSI design at an early stage as well as a later stage, and particularly a VLSI emulator based on processors and reconfigurable chips. The model of the VLSI chip is divided into a functional part and an external interface part. The functional part is executed by a processing module having at least one processor, and the external interface part is executed by an external interface signal processor to generate real pin signals. The external interface part is implemented using reconfigurable circuits by programming the circuits. The communicating between the functional part and the external interface part is accomplished by transmitting and/or receiving control packets composed of control commands and/or data. The internal functional part and the external interface part are verified on a target system at an early stage of the VLSI design, which may reduce time for designing the VLSI and verifying and designing whole system.

IPC 1-7

G06F 9/455; G06F 17/00

IPC 8 full level

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CPC (source: EP KR)

G06F 9/455 (2013.01 - KR); **G06F 30/33** (2020.01 - EP); **G06F 30/331** (2020.01 - EP)

Citation (search report)

See references of WO 0057273A1

Citation (examination)

- EP 0838772 A2 19980429 - QUICKTURN DESIGN SYSTEMS INC [US]
- EP 0777180 A2 19970604 - EAGLE DESIGN AUTOMATION INC [US]
- US 5479355 A 19951226 - HYDUKE STANLEY M [US]

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