Title (en)

METHOD AND APPARATUS FOR PROGRAMMING A GRAPHICS SUBSYSTEM REGISTER SET

Title (de)

VERFAHREN UND GERÄT ZUM PROGAMMIERENEINES GRAFISCHEN UNTERSYSTEM-REGISTERSETS

Title (fr)

PROCEDE ET APPAREIL POUR PROGRAMMER UN ENSEMBLE DE REGISTRES DANS UN PROCESSEUR GRAPHIQUE

Publication

EP 1080413 A1 20010307 (EN)

Application

EP 98921155 A 19980512

Priority

US 9809688 W 19980512

Abstract (en)

[origin: WO9959068A1] A graphics system (Figure 3) includes a graphics processor (310) for rendering graphics primitives with a display list. A host processor (302) generates a display list which includes a command format for loading the display list into a register file (312). The graphics processor (310) includes logic (410) to encode and decode the command register to sequentially load the display list into the register file (312) without a physical reference to the register being loaded. The command register may also be programmed to allow the gaphics processor (310) to randomly load register file (312) thereby shortening the processing of the display list and allowing the display list to be written during a burst cycle mode of bus operation.

IPC 1-7

G06F 12/06

IPC 8 full level

G06F 13/14 (2006.01); **G06T 1/20** (2006.01); **G06F 12/06** (2006.01); **G06F 13/28** (2006.01)

CPC (source: EP)

G06T 1/20 (2013.01)

Designated contracting state (EPC)

GB

DOCDB simple family (publication)

WO 9959068 A1 19991118; AU 7383198 A 19991129; EP 1080413 A1 20010307; EP 1080413 A4 20060201; JP 2002514817 A 20020521; JP 4846097 B2 20111228

DOCDB simple family (application)

US 9809688 W 19980512; AU 7383198 A 19980512; EP 98921155 A 19980512; JP 2000548810 A 19980512