

Title (en)
METHOD AND APPARATUS FOR STORING AND ACCESSING DIFFERENT CHIP SEQUENCES

Title (de)
VERFAHREN UND VORRICHTUNG ZUR SPEICHERUNG VON VERSCHIEDENEN SPREIZ-FOLGEN UND ZUM ZUGRIFF AUF DIESELBEN

Title (fr)
PROCEDE ET DISPOSITIF SERVANT A MEMORISER DIFFERENTES SEQUENCES D'ELEMENTS ET A ACCEDER A CES SEQUENCES

Publication
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Application
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Priority

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Abstract (en)
[origin: WO9967895A1] A chip sequence generator (160) stores sequences in a memory (190) having a memory address system. A chip sequence reader uses a memory read address generator (150) to access different phases of the sequences. The reader delivers the different phases to correlators or circuits in a spread spectrum receiver. The memory read address generator generates addresses dependant on a finger select value and a counter value. Each finger select value corresponds to a particular phase of a sequence. The counter value corresponds to a position in time. In sleep mode, the counter value is changed to correspond to the number of clock cycles in a sleep period. The receiver conserves power during sleep and accesses the correct phase of the sequence at wake up. The memory can also store paging sequences. The chip sequence generator generates new sequences as needed and writes them to addresses used to store sequences no longer needed.

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