

Title (en)
SUBSTRATE FOR HIGH-VOLTAGE MODULES

Title (de)
SUBSTRAT FÜR HOCHSPANNUNGSMODULE

Title (fr)
SUBSTRAT DE MODULES HAUTE TENSION

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Application
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Abstract (en)
[origin: WO0008686A2] The invention relates to a substrate for high-voltage modules comprising a ceramic layer with a first main side and a second main side having a first dielectric constant and being located opposite to the first main side. A top metal layer is disposed on the first main side while a bottom metal layer is disposed on the second main side. To reduce field tips, the invention provides for a dielectric layer having a second dielectric constant bordering on the top metal layer and disposed on the first main side of the ceramic layer. The density of the field lines on the edges of the voltage-conducting elements is thus attenuated so that the dielectric constants of the ceramic layer and the dielectric layer match each other.

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