

Title (en)
CURRENT-CONTROLLED OUTPUT BUFFER

Title (de)
STROMGESTEUERTER AUSGANGSPUFFER

Title (fr)
TAMPON DE SORTIE COMMANDE PAR COURANT

Publication
EP 1105968 A1 20010613 (EN)

Application
EP 99930294 A 19990615

Priority

- US 9913529 W 19990615
- US 9788698 A 19980615
- US 9760598 A 19980615

Abstract (en)
[origin: WO9966639A1] A current-controlled output buffer circuit includes a control circuit (110), a charging circuit (120), and a discharging circuit (150). The control circuit is configured to receive a control signal (102), and in response produces a charging signal (106) and discharging signal (108). The charging circuit is configured to receive the charging signal and in response, supplies a charging current to an output terminal, the magnitude of the charging current producing a signal rise time. The discharging circuit is configured to receive the discharging signal and in response, sinks a discharging current from the output terminal, the magnitude of the discharging current from producing a signal fall time.

IPC 1-7
H03K 3/00

IPC 8 full level
G01R 27/26 (2006.01); **H03K 17/16** (2006.01); **H03K 19/00** (2006.01); **H03K 19/003** (2006.01)

CPC (source: EP)
G01R 27/2605 (2013.01); **H03K 17/163** (2013.01); **H03K 19/0005** (2013.01); **H03K 19/00361** (2013.01)

Designated contracting state (EPC)
DE GB

DOCDB simple family (publication)
WO 9966639 A1 19991223; DE 69935226 D1 20070405; DE 69935226 T2 20080103; EP 1097386 A1 20010509; EP 1097386 A4 20010912;
EP 1097386 B1 20070221; EP 1105968 A1 20010613; EP 1105968 A4 20050615; WO 9966336 A1 19991223

DOCDB simple family (application)
US 9913529 W 19990615; DE 69935226 T 19990615; EP 99930293 A 19990615; EP 99930294 A 19990615; US 9913528 W 19990615