Title (en)

EXPLICIT RATE MARKING FOR FLOW CONTROL IN ATM NETWORKS

Title (de)

MARKIEREN DER EXPLIZITEN RATE ZUR FLUSSTEUERUNG IN ATM-NETZEN

Title (fr)

MARQUAGE DE DEBIT EXPLICITE POUR CONTROLE DE FLUX DANS DES RESEAUX ATM

Publication

**EP 1110353 A4 20050817 (EN)**

Application

**EP 99924274 A 19990517**

Priority

• US 9910807 W 19990517
• US 8558998 P 19980515

Abstract (en)

[origin: WO9960758A1] An explicit rate marking system comprises virtual bandwidth value generator module and an explicit rate value generator module. The virtual bandwidth value generator module generates a virtual bandwidth value reflecting an available bandwidth capacity value, an explicit rate value associated with each bottlenecked virtual circuit for which the switching node (12) forms part of a path (13) and a minimum cell rate value associated with each unbottlenecked virtual circuit for which the switching node (12) forms part of a path (13). The explicit rate value generator module generates the explicit rate value in relation to the virtual bandwidth value and the minimum cell rate value associated with the virtual circuit and other virtual circuits for which the switching node (12) forms part of the path (13).

IPC 1-7

**H04L 12/56**

IPC 8 full level

**H04L 12/54** (2013.01); **H04L 49/111** (2022.01); **H04Q 11/04** (2006.01)

CPC (source: EP)

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Citation (search report)

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• [A] KALAMPOUKAS L ET AL INTERNATIONAL FEDERATION FOR INFORMATION PROCESSING (IFIP): "AN EFFICIENT RATE ALLOCATION ALGORITHM FOR ATM NETWORKS PROVIDING MAX-MIN FAIRNESS", HIGH PERFORMANCE NETWORKING 6. IFIP 6TH. INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE NETWORKING. (HPN). PALMA DE MALLORCA, SEPT. 13 - 15, 1995, INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE NETWORKING (HPN), LONDON, CHAPMAN & HALL, GB, vol. CONF. 6, 11 September 1995 (1995-09-11), pages 143 - 154, XP000624333, ISBN: 0-412-73290-4
• See references of WO 9960758A1

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