

Title (en)
Low loss voltage preregulator

Title (de)
Verlustarmer vorgeschalteter Spannungsregler

Title (fr)
Prérégulateur de tension à faibles pertes

Publication
EP 1111492 A1 20010627 (EN)

Application
EP 00309544 A 20001030

Priority
US 45061799 A 19991130

Abstract (en)
A preregulator circuit is connected between the battery supply of a cellular phone and the input of an integrated circuit regulator to maintain the input of the integrated circuit at a voltage consistent with its rated voltage. To avoid the voltage drop in the preregulator when the battery is low, a shut off circuit is actuated to short circuit or minimize the voltage drop of the preregulator. <IMAGE>

IPC 1-7
G05F 1/575; **G05F 1/56**

IPC 8 full level
H02J 7/00 (2006.01); **G05F 1/10** (2006.01); **G05F 1/56** (2006.01); **G05F 1/575** (2006.01); **H02J 1/00** (2006.01)

CPC (source: EP)
G05F 1/575 (2013.01)

Citation (search report)
• [XA] US 5629609 A 19970513 - NGUYEN BAOSON [US], et al
• [A] US 5982158 A 19991109 - SCHNARS MICHAEL JOHN [US], et al
• [A] US 4536699 A 19850820 - BAKER RICHARD H [US]
• [A] US 4543522 A 19850924 - MOREAU JEAN M [FR]

Cited by
DE102009052836A1; CN107171371A; GB2364578A; US9516711B2; US8013587B2

Designated contracting state (EPC)
DE FR GB SE

DOCDB simple family (publication)
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DOCDB simple family (application)
EP 00309544 A 20001030; JP 2000365065 A 20001130