

Title (en)
DEVICES AND TECHNIQUES FOR LOGICAL PROCESSING

Title (de)
VORRICHTUNGEN UND TECHNIKEN FÜR LOGISCHES VERARBEITEN

Title (fr)
DISPOSITIFS ET PROCEDES DE TRAITEMENT LOGIQUE

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EP 1116142 A1 20010718 (EN)

Application
EP 99948387 A 19990922

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Abstract (en)
[origin: WO0017788A1] The invention is directed to apparatus, methods, systems and computer program products which permit a simplification of the logic required for performing a certain function to a minimum set of logical elements of operations, permitting a complex digital circuitry to be simplified so that the processing speed for performing the complex digital operations, is reduced. This is accomplished by using a system of propositional logic (13B), representing the logic of a logical circuit to be designed as points and vectors in a vector space, simplifying the logic of the logical circuit to a simpler form using the points and vectors in a vector space, and designing the circuit using the simpler form.

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