

Title (en)

INTERCONNECTION FOR INTEGRATED HIGH VOLTAGE ELECTRONIC DEVICES

Title (de)

VERDRAHTUNG INTEGRIERTER HOCHSPANNUNGSSCHALTUNGEN

Title (fr)

INTERCONNEXION DE DISPOSITIFS ELECTRONIQUES INTEGRES HAUTE TENSION

Publication

**EP 1116272 A1 20010718 (FR)**

Application

**EP 99929441 A 19990708**

Priority

- FR 9901661 W 19990708
- FR 9808820 A 19980709

Abstract (en)

[origin: FR2781083A1] The invention concerns a device for interconnecting at least two electronic elements (2, 4) separated by at least a buffer zone (10, 12), the interconnection (18) comprising one or more interconnection path(s) for the elements, open-worked (20) at least in the parts of the path(s) located above the buffer zones.

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**H01L 23/528**

IPC 8 full level

**H01L 23/528** (2006.01)

CPC (source: EP US)

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