Title (en)

FREQUENCY DETECTION METHOD FOR CLOCK SIGNAL ADJUSTMENT AND FREQUENCY DETECTION CIRCUIT FOR IMPLEMENTING SAID METHOD

Title (de)

FREQUENZDETEKTIONSVERFAHREN ZUR TAKTSIGNALFREQUENZ-NACHSTELLUNG UND FREQUENZDETEKTORSCHALTUNG ZUR DURCHFÜHRUNG DES VERFAHRENS

Title (fr)

PROCEDE DE DETECTION DE FREQUENCE DESTINE A L'AJUSTEMENT D'UNE FREQUENCE DE SIGNAL D'HORLOGE ET CIRCUIT DE DETECTION DE FREQUENCE PERMETTANT DE METTRE EN OEUVRE LEDIT PROCEDE

Publication

EP 1116329 A1 20010718 (DE)

Application

EP 99953679 A 19990901

Priority

- DE 9902766 W 19990901
- DE 19844126 A 19980925

Abstract (en)

[origin: WO0019613A1] Frequency detection method for adjusting the clock signal frequency to the data rate of a received data signals, wherein the clock signal is previously divided by 4 and the received data signal are divided on a frequency basis with the same division factor, the frequencies of both frequency divided signals are determined through pulse-count processes and compared using a subtractor (10), wherein the determined frequency difference is converted into an analog output signal to regulate clock signal frequency. Said method can be used in data transmission.

IPC 1-7

H03L 7/113; H04L 7/033

IPC 8 full level

H03K 5/26 (2006.01); **H03L 7/08** (2006.01); **H03L 7/087** (2006.01); **H03L 7/113** (2006.01); **H04L 7/033** (2006.01); H03L 7/095 (2006.01)

CPC (source: EP US)

H03L 7/113 (2013.01 - EP US); **H04L 7/033** (2013.01 - EP US); H03L 7/087 (2013.01 - EP US); H03L 7/095 (2013.01 - EP US)

Citation (search report)

See references of WO 0019613A1

Designated contracting state (EPC)

FR IT

DOCDB simple family (publication)

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DOCDB simple family (application)

DE 9902766 W 19990901; DE 19844126 A 19980925; EP 99953679 A 19990901; JP 2000573001 A 19990901; US 81784101 A 20010326