

Title (en)
VIDEO CONFERENCING INTERFACE

Title (de)
VIDEOKONFERENZSCHNITTSTELLE

Title (fr)
INTERFACE POUR SYSTEME DE VIDEOCONFERENCE

Publication
EP 1116384 A4 20040818 (EN)

Application
EP 99937533 A 19990727

Priority
• US 9916995 W 19990727
• US 9464698 P 19980730

Abstract (en)
[origin: WO0007371A1] A video conferencing circuit (12) is configured to receive an input (26) from one of a plurality of video input devices. The video signal is then stored, compressed and transmitted by an interface circuit such as a modem (18). Video signals from a remote location are received from the modem (18), decompressed, stored and then transferred for display on one of a plurality of video output devices.

IPC 1-7
H04N 7/14

IPC 8 full level
H04N 7/15 (2006.01); **H04N 7/14** (2006.01)

CPC (source: EP KR)
H04N 7/148 (2013.01 - EP); **H04N 7/15** (2013.01 - KR)

Citation (search report)
• [Y] US 5367331 A 19941122 - SECHER FERNAND [FR], et al
• [Y] US 5539452 A 19960723 - BUSH DANIEL R [US], et al
• [A] US 5457780 A 19951010 - SHAW VENSON M [US], et al
• [A] WO 9632717 A1 19961017 - SIEMENS AG [DE], et al
• [A] WO 9608110 A1 19960314 - RSI SYSTEMS INC [US]
• [A] PATENT ABSTRACTS OF JAPAN vol. 0176, no. 20 (E - 1460) 16 November 1993 (1993-11-16)
• See references of WO 0007371A1

Designated contracting state (EPC)
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)
WO 0007371 A1 20000210; AU 5234699 A 20000221; AU 752570 B2 20020926; CA 2338195 A1 20000210; CA 2338195 C 20081104; EP 1116384 A1 20010718; EP 1116384 A4 20040818; IL 141005 A0 20020210; IL 141005 A 20060705; JP 2002521979 A 20020716; KR 20010072118 A 20010731

DOCDB simple family (application)
US 9916995 W 19990727; AU 5234699 A 19990727; CA 2338195 A 19990727; EP 99937533 A 19990727; IL 14100501 A 20010122; IL 14100599 A 19990727; JP 2000563070 A 19990727; KR 20017001288 A 20010130