

Title (en)

SWITCHING TRANSISTOR WITH REDUCED SWITCHING LOSSES

Title (de)

GESCHALTETES NETZTEIL MIT REDUZIERTEN SCHALTVERLUSTEN

Title (fr)

ALIMENTATION A DECOUPAGE PRESENTANT DES PERTES DE COMMUTATION REDUITES

Publication

**EP 1119876 A2 20010801 (DE)**

Application

**EP 99969183 A 19990910**

Priority

- DE 9902874 W 19990910
- DE 19841754 A 19980911

Abstract (en)

[origin: DE19841754A1] The invention relates to a switching transistor presenting reduced switching losses. In said switching transistor output capacitance is very high when drain/source voltages are low. As the drain/source voltage increases said capacitance falls to such low values that the energy stored in the transistor becomes very low.

IPC 1-7

**H01L 29/78**; **H02M 3/155**

IPC 8 full level

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CPC (source: EP US)

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Citation (search report)

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