

Title (en)

ELECTRONIC COMPONENT FOR MASKING EXECUTION OF INSTRUCTIONS OR DATA MANIPULATION

Title (de)

ELEKTRONISCHES BAUELEMENT UND VERFAHREN ZUM MASKIEREN VON DER AUSFÜHRUNG VON BEFEHLEN ODER DER MANIPULATION VON DATEN

Title (fr)

COMPOSANT ELECTRONIQUE ET PROCEDE POUR MASQUER L'EXECUTION D'INSTRUCTIONS OU LA MANIPULATION DE DONNEES

Publication

**EP 1121629 A1 20010808 (FR)**

Application

**EP 99949058 A 19991015**

Priority

- FR 9902521 W 19991015
- FR 9812988 A 19981016

Abstract (en)

[origin: FR2784763A1] The electronic component (CI) has a microprocessor with an interruption port (7), a program memory (2), a working memory (3), a counter (4) and a random number generator (5). At the start of operations the random number generator produces a number (R) which loads the counter. When the counter reaches zero the microprocessor is interrupted and a secondary program of constant, variable or random length is run.

IPC 1-7

**G06F 1/00**

IPC 8 full level

**G06F 12/14** (2006.01); **G06F 21/55** (2013.01); **G06F 21/75** (2013.01); **G06K 19/073** (2006.01)

CPC (source: EP US)

**G06F 21/755** (2017.07 - EP US); **H04L 9/003** (2013.01 - EP); **G06F 2207/7219** (2013.01 - EP); **G06F 2221/2123** (2013.01 - EP)

Citation (search report)

See references of WO 0023866A1

Designated contracting state (EPC)

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