Title (en)

Column decoder circuit for page reading of a semiconductor memory

Title (de)

Spaltedekodierer für das Lesen von Seiten in einem Halbleiterspeicher

Title (fr)

Décodeur de colonne pour lecture en mode de page dans une mémoire à semiconducteurs

Publication

EP 1130601 B1 20050126 (EN)

Application

EP 00830149 A 20000229

Priority

EP 00830149 A 20000229

Abstract (en)

[origin: EP1130601A1] A column decoder circuit for page reading of a semiconductor memory includes a first level decoder stage (21), a second level decoder stage (22), and a plurality of bit selection stages (24), each comprising a plurality of selection branches (32a, 32b, 32c, 32d); wherein each selection branch is connected to a respective input of a multiplexer (33) and has a plurality of first level selector stages (35) and a second level selector stage (36, 48). Each second level selector stage (36, 48) comprises a first addressing selector (41) for addressing a first group of bit lines (30). Each bit selection stage (24) further comprises a second addressing selector (42) for addressing a second group of bit lines (30), current and next page selectors (45, 46) for selecting one of the first and second groups of bit lines (30). <IMAGE> <IMAGE>

IPC 1-7

G11C 8/00; G11C 7/10

IPC 8 full level

G11C 7/10 (2006.01); **G11C 8/10** (2006.01)

CPC (source: EP US)

G11C 7/1021 (2013.01 - EP US); **G11C 8/10** (2013.01 - EP US)

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DOCDB simple family (publication)

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DOCDB simple family (application)

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