

Title (en)

DEVICE FOR LIMITING FRAUD IN AN INTEGRATED CIRCUIT CARD

Title (de)

VORRICHTUNG ZUR BETRUGSVERHINDERUNG EINER CHIPKARTE

Title (fr)

DISPOSITIF POUR LA LIMITATION DE FRAUDES DANS UNE CARTE A CIRCUIT INTEGRE

Publication

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Application

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Priority

- FR 9902690 W 19991104
- FR 9814409 A 19981117

Abstract (en)

[origin: FR2786006A1] The invention concerns an integrated circuit device containing a storage zone comprising a data storage unit. The invention is characterised in that said data storage unit comprises at least a counter element, at least an indicator element and at least a threshold value, said counter element counting at least the number of events having occurred in the device, and being capable of reaching said threshold value indicating a high maximum number of occurrences of said events, said indicator element being capable of passing from one first state to a second state when said counter element has reached said threshold value. The invention is applicable to smart cards.

IPC 1-7

G07F 7/10

IPC 8 full level

G06K 19/10 (2006.01); **G07F 7/10** (2006.01)

CPC (source: EP US)

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Citation (search report)

See references of WO 0030047A1

Designated contracting state (EPC)

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FR 9814409 A 19981117; AU 1051300 A 19991104; CN 99813415 A 19991104; EP 99954054 A 19991104; FR 9902690 W 19991104; JP 2000582978 A 19991104; US 85619101 A 20010813