

Title (en)
PROCESSING CIRCUIT FOR CORRECTING AUDIO SIGNALS, RECEIVER, COMMUNICATION SYSTEM, MOBILE APPARATUS AND RELATED METHOD

Title (de)
VERARBEITUNGSSCHALTKREIS FÜR FEHLERKORREKTUR VON AUDIOSIGNALLEN, EMPFÄNGER, FERNMELDESYSTEM, MOBILES ENDGERÄT, SOWIE VERFAHREN HIERFÜR

Title (fr)
CIRCUIT DE TRAITEMENT POUR CORRIGER LES SIGNAUX AUDIO, RECEPTEUR, SYSTEME DE COMMUNICATIONS, APPAREIL MOBILE ET PROCEDE CORRESPONDANT

Publication
EP 1131815 A1 20010912 (EN)

Application
EP 00971279 A 20000908

Priority
• EP 00971279 A 20000908
• EP 0008874 W 20000908
• EP 99402298 A 19990920

Abstract (en)
[origin: WO0122401A1] Transmission of audio signals, partitioned into frames, between a transmitter and a receiver may induce annoying artifacts in the signal. The invention aims at reducing these artifacts in the decoded frames by derivation of a magnitude (CAL) representative of the energy and comparison of this magnitude with a derived estimate (EST). A frame is corrected (CORR) when the magnitude exceeds in a great amount the estimate (DSP).

IPC 1-7
G10L 19/00

IPC 8 full level
G10L 21/02 (2006.01); **G10L 19/00** (2006.01); **G10L 19/005** (2013.01); **H04B 7/26** (2006.01); **H04M 1/00** (2006.01); **H04M 1/253** (2006.01); **H04M 1/725** (2006.01)

CPC (source: EP KR)
G10L 19/005 (2013.01 - EP); **H04B 1/06** (2013.01 - KR)

Citation (search report)
See references of WO 0122401A1

Designated contracting state (EPC)
DE FR GB IT

DOCDB simple family (publication)
WO 0122401 A1 20010329; CN 1322347 A 20011114; EP 1131815 A1 20010912; JP 2003510643 A 20030318; KR 20010080476 A 20010822

DOCDB simple family (application)
EP 0008874 W 20000908; CN 00802016 A 20000908; EP 00971279 A 20000908; JP 2001525685 A 20000908; KR 20017006239 A 20010517