

Title (en)  
CONSERVATION OF SYSTEM MEMORY BANDWIDTH AND CACHE COHERENCY MAINTENANCE USING MEMORY CANCEL MESSAGES

Title (de)  
EINSPARUNG DER SYSTEMSPEICHERBANDBREITE UND AUFRECHTERHALTUNG DER CACHESPEICHERKOHÄRENZ MIT  
SPEICHERANNULIERUNGSNACHRICHTEN

Title (fr)  
CONSERVATION DE LA LARGEUR DE BANDE DE MEMOIRE SYSTEME ET MAINTENANCE DE LA COHERENCE D'ANTEMEMOIRES A  
L'AIDE DE MESSAGES D'EFFACEMENT DE MEMOIRE

Publication  
**EP 1141838 A1 20011010 (EN)**

Application  
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- US 21769998 A 19981221
- US 21721298 A 19981221
- US 21764998 A 19981221
- US 37097099 A 19990810

Abstract (en)  
[origin: WO0038070A1] A messaging scheme that conserves system memory bandwidth and maintains cache coherency during a victim block write operation in a multiprocessing computer system (10) is described. A target node (72) receives a memory cancel response corresponding to a transaction, and aborts processing of the transaction in response to the memory cancel response. In one embodiment, the transaction is a victim block write and the memory cancel response is received from a source node (70). In another embodiment, the transaction is a read operation and the memory cancel response is received from a different node (76) having a modified copy of the data addressed by the read operation.

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**G06F 12/08**

IPC 8 full level  
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