

Title (en)

A SYNCHRONOUS FLYBACK CONVERTER

Title (de)

SYNCHRONER SPERRWANDLER

Title (fr)

CONVERTISSEUR INDIRECT SYNCHRONE

Publication

EP 1145415 A1 20011017 (EN)

Application

EP 99964889 A 19991216

Priority

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- SE 9804454 A 19981221

Abstract (en)

[origin: WO0038305A1] In a continuous mode flyback converter having a synchronous switch (Q2) the drive pulse to the switch (Q2) on the secondary side is generated by an inverting buffer circuit being fed from the output voltage terminal. By using such an arrangement several advantages are obtained. Hence, the pulse generating circuit arrangement generates a drive signal to the synchronous switch which is independent of the input voltage and the drive losses can thereby be minimized.

IPC 1-7

H02M 3/335

IPC 8 full level

H02M 7/12 (2006.01); **H02M 3/28** (2006.01); **H02M 3/335** (2006.01); **H02M 7/21** (2006.01)

IPC 8 main group level

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DOCDB simple family (publication)

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