

Title (en)

Voltage regulator with low power consumption

Title (de)

Spannungsregler mit niedrigem Energieverbrauch

Title (fr)

Régulateur de tension à faible consommation électrique

Publication

EP 1148404 B1 20051102 (FR)

Application

EP 01108243 A 20010331

Priority

FR 0004675 A 20000412

Abstract (en)

[origin: US2001030530A1] A voltage regulator includes a regulation MOS transistor and an amplifier providing an output for driving a gate of the regulation MOS transistor. The amplifier drives the gate based upon a difference between a reference voltage and a feedback voltage. The voltage regulator may further include a circuit for making the amplifier switch to a standby mode with low current consumption when the difference between the supply voltage and the output voltage of the regulator is below a first threshold. This is done while maintaining, at the gate of the regulation transistor, a voltage that keeps the regulation transistor on. The present invention is particularly applicable to the management of power supplies in portable telephones.

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G05F 1/565

IPC 8 full level

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