

Title (en)

Linear regulator with low over-voltage in transient-state

Title (de)

Linearer Regler mit niedriger Überspannung im Übergangszustand

Title (fr)

Régulateur linéaire à faible surtension en régime transitoire

Publication

**EP 1148405 B1 20060607 (FR)**

Application

**EP 01108258 A 20010331**

Priority

FR 0004673 A 20000412

Abstract (en)

[origin: US2001050546A1] A voltage regulator includes a regulation MOS transistor with low serial resistance having a first terminal connected to a voltage source and a second terminal connected to the output of the voltage regulator and an amplifier having an output driving a gate of the transistor. The gate is driven based upon a difference between a reference voltage and a feedback voltage. The regulator may also include an anti-overshoot switch with a first terminal connected to the gate of the regulation MOS transistor and a second terminal is taken to a potential for turning the regulation MOS transistor off. A switch controller closes the switch when the output voltage of the regulator is higher than a first threshold. The first threshold may be higher than the nominal value of the output voltage.

IPC 8 full level

**G05F 1/565** (2006.01); **G05F 1/575** (2006.01)

CPC (source: EP US)

**G05F 1/565** (2013.01 - EP US); **G05F 1/575** (2013.01 - EP US)

Cited by

CN104898754A; EP2846213A1

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

**EP 1148405 A1 20011024**; **EP 1148405 B1 20060607**; DE 60120270 D1 20060720; FR 2807847 A1 20011019; FR 2807847 B1 20021122; US 2001050546 A1 20011213; US 6388433 B2 20020514

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