

Title (en)  
Debugging device and method

Title (de)  
Vorrichtung und Verfahren zur Fehlerbeseitigung

Title (fr)  
Dispositif et procédé de débogage

Publication  
**EP 1148420 A1 20011024 (EN)**

Application  
**EP 01300902 A 20010201**

Priority  
GB 0009945 A 20000420

Abstract (en)  
During debugging of target system by a host system, a single stack is used for an exception by a set of applications running on the processor of the target. To achieve this, the stack is dynamically loaded by the host to a reserved memory region, and a vector of the target is set to point to that reserved region. The exception handlers of each application then use the vector to access the stack.

IPC 1-7  
**G06F 11/36**

IPC 8 full level  
**G06F 11/36** (2006.01)

CPC (source: EP US)  
**G06F 11/3656** (2013.01 - EP US)

Citation (search report)  
• [A] US 5394544 A 19950228 - MOTOYAMA TETSURO [US], et al  
• [A] US 5715387 A 19980203 - BARNSTIJN MICHAEL A [CA], et al

Designated contracting state (EPC)  
DE FR GB IT

DOCDB simple family (publication)  
**EP 1148420 A1 20011024**; GB 0009945 D0 20000607; US 2002059560 A1 20020516; US 7039831 B2 20060502

DOCDB simple family (application)  
**EP 01300902 A 20010201**; GB 0009945 A 20000420; US 77846601 A 20010207