

Title (en)

RAPID ON CHIP VOLTAGE GENERATION FOR LOW POWER INTEGRATED CIRCUITS

Title (de)

SCHNELLE SPANNUNGSERZEUGUNG AUF DEM CHIP FÜR INTEGRIERTE SCHALTUNGEN NIEDRIGER LEISTUNG

Title (fr)

GENERATION RAPIDE DE TENSION DANS LE MICROCIRCUIT DANS LE CAS DE CIRCUITS INTEGRES COURANT FAIBLE

Publication

EP 1151365 B1 20040512 (EN)

Application

EP 98960305 A 19981118

Priority

US 9824766 W 19981118

Abstract (en)

[origin: WO0029919A1] An on chip voltage generation circuit is provided for use on integrated circuits such as flash memory devices with a low power supply voltage. The voltage generation circuit comprises first and second level detectors (209 and 210) and a boost driver (204), wherein the voltage generation circuit pumps charge at first and second rates in response to outputs of the detectors.

IPC 1-7

G05F 1/10; **G05F 3/02**; **H01L 27/02**; **G11C 5/14**; **G05F 3/20**; **G05F 1/46**

IPC 8 full level

G11C 11/413 (2006.01); **B60K 20/00** (2006.01); **C08G 59/06** (2006.01); **F02M 61/10** (2006.01); **G05F 1/10** (2006.01); **G05F 1/46** (2006.01); **G05F 3/02** (2006.01); **G05F 3/20** (2006.01); **G11C 5/14** (2006.01); **G11C 11/407** (2006.01); **G11C 16/06** (2006.01); **H01L 21/822** (2006.01); **H01L 27/02** (2006.01); **H01L 27/04** (2006.01)

IPC 8 main group level

G05F (2006.01)

CPC (source: EP)

G05F 1/46 (2013.01)

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