

Title (en)
COMPARATOR CIRCUIT

Title (de)
KOMPARATORSCHALTUNG

Title (fr)
CIRCUIT COMPAREUR

Publication
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Application
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Abstract (en)
[origin: WO0048315A1] The invention relates to a comparator circuit comprising a comparator which is provided with an operating point T1. A supervisory circuit suppresses undesired signal bouncing at the output. Said bouncing is produced by interferences of the input signal. The supervisory circuit ensures that the circuit output is locked from being switched back to another logical level after switching until the input signal has reached a second threshold value T2 that is higher than the operating point T1.

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IPC 8 full level
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