Title (en)

AUTOMATED PROCESSOR GENERATION SYSTEM & METHOD FOR DESIGNING A CONFIGURABLE PROCESSOR

Title (de)

AUTOMATISCHES PROZESSORERZEUGUNGSSYSTEM UND -VERFAHREN ZUR ENTWURF EINES KONFIGURIERBAREN PROZESSORS

Title (fr)

SYSTEME DE GENERATION D'UN PROCESSEUR AUTOMATISE DESTINE A LA CONCEPTION D'UN PROCESSEUR CONFIGURABLE ET PROCEDE CONNEXE

Publication

EP 1159693 A2 20011205 (EN)

Application

EP 00913380 A 20000204

Priority

- US 0003091 W 20000204
- US 24604799 A 19990205
- US 32316199 A 19990527
- US 32273599 A 19990528

Abstract (en)

[origin: WO0046704A2] A configurable RISC processor implements a user-definable instruction set with high performance fixed and variable length encoding. The process of defining new instruction sets is supported by tools that allow the user to add new instructions and rapidly evaluate them, to maintain multiple instruction sets & to easily switch between them. A standardized language is used to develop configurable definitions of target instructions sets, HDL descriptions of hardware needed to implement the instruction set, and development tools for verification and application development, thus enabling a high degree of automation in the design process.

IPC 1-7

G06F 17/50

IPC 8 full level

G06F 9/45 (2006.01); **G06F 11/28** (2006.01); **G06F 17/50** (2006.01)

CPC (source: EP KR US)

G06F 8/20 (2013.01 - KR); **G06F 8/37** (2013.01 - KR); **G06F 8/40** (2013.01 - KR); **G06F 9/455** (2013.01 - KR); **G06F 11/28** (2013.01 - KR US); **G06F 11/3006** (2013.01 - KR); **G06F 11/362** (2013.01 - KR); **G06F 13/12** (2013.01 - KR); **G06F 30/00** (2020.01 - KR); **G06F 30/30** (2020.01 - EP KR); **G06F 30/33** (2020.01 - EP KR); **G06F 30/3308** (2020.01 - US)

Citation (search report)

See references of WO 0046704A2

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Designated contracting state (EPC)

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)

WO 0046704 A2 20000810; **WO 0046704 A3 20001214**; AU 3484100 A 20000825; CN 1382280 A 20021127; EP 1159693 A2 20011205; JP 2003518280 A 20030603; JP 2007250010 A 20070927; KR 100775547 B1 20071109; KR 100874738 B1 20081222; KR 20020021081 A 20020318; KR 20070088818 A 20070829; TW 539965 B 20030701

DOCDB simple family (application)

US 0003091 W 20000204; AU 3484100 A 20000204; CN 00812731 A 20000204; EP 00913380 A 20000204; JP 2000597714 A 20000204; JP 2007161932 A 20070619; KR 20017009857 A 20010803; KR 20077017999 A 20070803; TW 89102150 A 20000310