Title (en)

DIGITAL ELECTRONIC METHOD FOR INCREASING THE CALCULATION ACCURACY IN NON-LINEAR FUNCTIONS AND HARDWARE ARCHITECTURE FOR CARRYING OUT SAID METHOD

Title (de)

DIGITAL-ELEKTRONISCHES VERFAHREN ZUR STEIGERUNG DER BERECHNUNGSGENAUIGKEIT BEI NICHTLINEAREN FUNKTIONEN UND EINE HARDWARE-ARCHITEKTUR ZUR DURCHFÜHRUNG DES VERFAHRENS

Title (fr)

PROCEDE NUMERIQUE-ELECTRONIQUE POUR AUGMENTER LA PRECISION DE CALCUL DANS DES FONCTIONS NON LINEAIRES ET ARCHITECTURE DE MATERIEL POUR METTRE LEDIT PROCEDE EN OEUVRE

Publication

EP 1161718 A1 20011212 (DE)

Application

EP 00905027 A 20000205

Priority

• DE 19906559 A 19990215
• EP 0000920 W 20000205

Abstract (en)

[origin: DE19906559C1] The calculation method has the number values for a non-linear function entered as an input word together with an associated codeword with a floating point input format (EFf), received by an input multiplexer (M1) of an electronic data processor and converted into an intermediate fixed point format (ZF), divided into a coding region (Kc) and a lower value position region (Gc), subsequently combined via an output multiplexer (M2), to provide the output format(AF). An Independent claim for a digital-electronic system is also included.

IPC 1-7

G06F 1/035

IPC 8 full level

G06F 1/03 (2006.01); **G06F 7/38** (2006.01); **G06F 9/30** (2006.01)

CPC (source: EP US)

G06F 1/03 (2013.01 - EP US); **G06F 7/38** (2013.01 - EP US); **G06F 9/30025** (2013.01 - EP US); G06N 3/048 (2023.01 - EP US); G06N 3/063 (2013.01 - EP US)

Citation (search report)

See references of WO 0049484A1

Designated contracting state (EPC)

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DOCDB simple family (publication)

DE 19906559 C1 20000420; EP 1161718 A1 20011212; JP 2002537595 A 20021105; US 2003208515 A1 20031106; US 6941329 B2 20050906; WO 0049484 A1 20000824

DOCDB simple family (application)

DE 19906559 A 19990215; EP 0000920 W 20000205; EP 00905027 A 20000205; JP 2000600163 A 20000205; US 90285501 A 20010712