

Title (en)

Small-scale and low-power consumption multipliers and filter circuits

Title (de)

Multiplizierer- und Filterschaltungen geringen Masstabs und mit geringem Leistungsverbrauch

Title (fr)

Circuits de multiplication et de filtrage à faible encombrement et consommation

Publication

**EP 1162563 A1 20011212 (EN)**

Application

**EP 00112437 A 20000609**

Priority

EP 00112437 A 20000609

Abstract (en)

A multiplication of digital data by small-scale and low-power consumption circuits. ANDs of all combinations of the respective bits of first digital data and the respective bits of second digital data are generated. A capacitive coupling is configured by a plurality of capacitances each having a capacity proportional to the sum of weights of pair of bits corresponding to each AND, an output of the capacitive coupling is connected to an inverting amplifier, and a multiplication result is output as analog data. <IMAGE>

IPC 1-7

**G06J 1/00**

IPC 8 full level

**G06J 1/00** (2006.01)

CPC (source: EP)

**G06J 1/00** (2013.01)

Citation (search report)

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TWI761147B

Designated contracting state (EPC)

DE FR GB NL

DOCDB simple family (publication)

**EP 1162563 A1 20011212**

DOCDB simple family (application)

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