Title (en)

METHODS AND APPARATUS FOR FACILITATING DIRECT MEMORY ACCESS

Title (de)

VERFAHREN UND GERÄT UM DIREKTEN SPEICHERZUGRIFF ZU ERLEICHTERN

Title (fr)

PROCEDES ET APPAREIL DESTINES A FACILITER L'ACCES MEMOIRE DIRECT

Publication

EP 1163596 A1 20011219 (EN)

Application

EP 00911873 A 20000218

Priority

- US 0004247 W 20000218
- US 12102299 P 19990222

Abstract (en)

[origin: WO0051004A1] Methods and apparatus for facilitating a direct memory access in a computing system are disclosed. In one embodiment (Figure), a computing system (100) having a central processing unit (CPU) (114) and a main system memory (106), a DMA engine (110) coupled to CPU (114) performs a first DMA process that identifies a desired data set. Once identified, the DMA engine (110) moves the identified desired data set from a main system memory (106) to a memory segment (115) that is temporally closer to the process or (114) than is the main system memory (106).

IPC 1-7

G06F 13/28

IPC 8 full level

G06F 13/28 (2006.01)

CPC (source: EP KR)

G06F 13/1673 (2013.01 - KR); **G06F 13/28** (2013.01 - EP KR)

Designated contracting state (EPC)

DE FR GB IE IT

DOCDB simple family (publication)

WO 0051004 A1 20000831; AU 3369700 A 20000914; CN 1153153 C 20040609; CN 1352773 A 20020605; EP 1163596 A1 20011219; EP 1163596 A4 20041201; JP 2002538522 A 20021112; KR 20010102285 A 20011115

DOCDB simple family (application)

US 0004247 W 20000218; AU 3369700 A 20000218; CN 00804161 A 20000218; EP 00911873 A 20000218; JP 2000601535 A 20000218; KR 20017010598 A 20010821