

Title (en)

CIRCUIT ARRANGEMENT FOR PROCESSING AN ATM CELL OVERHEAD

Title (de)

SCHALTUNGSANORDNUNG ZUM BEARBEITEN EINES ATM-ZELLENKOPFES

Title (fr)

CONFIGURATION DE CIRCUITS DESTINES AU TRAITEMENT D'UNE EN-TETE DE CELLULE MTA

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Application

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Abstract (en)

[origin: EP1039776A1] In current transmission systems, information is transmitted in ATM cells which are conveyed using several channels (SDH). In prior art, a processing circuit can be provided for each channel, in which test information that is representative of the cell overhead is generated, evaluated or corrected. The invention reduces the number of processing circuits by connecting FIFO memory units to said circuits. The memory units record the information concerning the cell overhead and feed said information to the processing circuits, in such a way that one processing circuit processes the cell overhead of ATM cells from several channels.

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