

Title (en)

CIRCUIT ARRANGEMENT FOR PROCESSING AN ATM CELL OVERHEAD

Title (de)

SCHALTUNGSANORDNUNG ZUM BEARBEITEN EINES ATM-ZELLENKOPFES

Title (fr)

CONFIGURATION DE CIRCUITS DESTINES AU TRAITEMENT D'UNE EN-TETE DE CELLULE MTA

Publication

**EP 1163820 A1 20011219 (DE)**

Application

**EP 00912533 A 20000301**

Priority

- EP 00912533 A 20000301
- EP 0001762 W 20000301
- EP 99105873 A 19990323

Abstract (en)

[origin: EP1039776A1] In current transmission systems, information is transmitted in ATM cells which are conveyed using several channels (SDH). In prior art, a processing circuit can be provided for each channel, in which test information that is representative of the cell overhead is generated, evaluated or corrected. The invention reduces the number of processing circuits by connecting FIFO memory units to said circuits. The memory units record the information concerning the cell overhead and feed said information to the processing circuits, in such a way that one processing circuit processes the cell overhead of ATM cells from several channels.

IPC 1-7

**H04Q 11/04**

IPC 8 full level

**H04L 49/111** (2022.01); **H04Q 11/04** (2006.01)

CPC (source: EP)

**H04L 49/3009** (2013.01); **H04L 49/3081** (2013.01); **H04Q 11/0478** (2013.01); **H04L 49/55** (2013.01); **H04L 2012/5647** (2013.01); **H04L 2012/5652** (2013.01); **H04L 2012/5672** (2013.01)

Citation (search report)

See references of WO 0057667A1

Designated contracting state (EPC)

BE DE FR GB NL

DOCDB simple family (publication)

**EP 1039776 A1 20000927**; CA 2367578 A1 20000928; EP 1163820 A1 20011219; WO 0057667 A1 20000928

DOCDB simple family (application)

**EP 99105873 A 19990323**; CA 2367578 A 20000301; EP 0001762 W 20000301; EP 00912533 A 20000301