Title (en)

Gradation control of a matrix display

Title (de)

Graustufensteuerung einer Matrixanzeige

Title (fr)

Commande de niveaux de gris sur un écran d'affichage du type matriciel

Publication

EP 1172790 A1 20020116 (EN)

Application

EP 01114898 A 20010702

Priority

• EP 01114898 A 20010702
• EP 00250231 A 20000713

Abstract (en)

The invention is related to an improved addressing scheme for plasma display panel control. In the conventional addressing scheme, the whole panel is treated as one section. This has the drawback that a long time distance is between writing a cell during addressing phase and light generation during the sustaining period. An improved addressing scheme has been proposed, where the panel is partitioned in a number of sections and correspondingly the addressing period is split up in corresponding small addressing periods for each section. Inbetween the small addressing periods there is some light generation, so that the time distance between addressing and light emission is substantially reduced. The proposal according to the invention further improves this addressing scheme by varying the time distance between the successive addressing periods of the sections from one sub-field to the other, at least for the sub-fields with higher weights. This leads to a better spread of the energy input and output of the plasma display panel, thus making it possible to use a power supply circuit with less expensive components. <IMAGE>

IPC 1-7

G09G 3/28

IPC 8 full level

G09G 3/20 (2006.01); G09G 3/293 (2013.01)

CPC (source: EP)

G09G 3/2022 (2013.01); G09G 3/293 (2013.01); G09G 2310/0216 (2013.01); G09G 2310/0218 (2013.01); G09G 2330/021 (2013.01)

Citation (search report)

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