

Title (en)
RECEIVER CIRCUIT

Title (de)
EMPFÄNGERSCHALTUNG

Title (fr)
CIRCUIT RECEPTEUR

Publication
EP 1177635 A1 20020206 (EN)

Application
EP 00930391 A 20000504

Priority
• GB 9910662 A 19990507
• US 0012298 W 20000504

Abstract (en)
[origin: GB2349783A] A radio frequency receiver circuit, for example for receiving digital terrestrial television broadcast signals. Such signals include wanted signals in wanted frequency bands, and may also include interference signals close to the wanted frequency bands. The receiver circuit receives a first intermediate frequency signal (10), and achieves downconversion by sub-sampling in an analog-digital converter (12). The sampling rate of the analog-digital converter is chosen relative to the intermediate frequency such that a known interference signal does not alias into the wanted frequency band, allowing it to be removed at baseband. The system may also include tuning circuits (6/16), automatic gain control (11), filtering (8/18) and demodulation (14).

IPC 1-7
H04B 1/26

IPC 8 full level
H04B 1/26 (2006.01); **H04B 1/28** (2006.01)

CPC (source: EP)
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Citation (search report)
See references of WO 0069083A1

Designated contracting state (EPC)
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