Title (en)

Flat plasma display panel with independent trigger and controlled sustaining electrodes

Title (de)

Flache Plasma-Anzeigetafel mit gesteuerten Plasma-Aufrechterhaltungselektroden

Title (fr)

Panneau plat d'affichage à plasma avec des électrodes d'entretien commandées

Publication

EP 1178512 A3 20071121 (EN)

Application

EP 01118278 A 20010730

Priority

US 62911800 A 20000731

Abstract (en)

[origin: EP1178512A2] A plasma flat-panel display comprising a hermetically sealed gas filled enclosure. The enclosure includes a top glass substrate (12) having a first pair of parallel sustainer electrodes (22,23) deposited upon the first substrate, the first pair of sustainer electrodes including a first sustainer electrode (22) and a second sustainer electrode. At least one auxiliary electrode is deposited upon the first substrate parallel to the first pair of sustainer electrodes, the auxiliary electrode being adjacent to the first sustainer electrode in the first pair of sustainer electrodes. A second pair of parallel sustainer electrodes is deposited upon the first substrate parallel to the trigger electrodes, the second pair of sustainer electrodes including a first sustainer electrode and a second sustainer electrode, the sustainer electrode pair being oriented upon the first substrate as a mirror image of the first sustainer electrode pair such that the first sustainer electrode in the second pair of sustainer electrodes is adjacent to the auxiliary electrode. A single common first sustainer electrode pad is electrically connected to the first sustainer electrode in the first sustainer electrode pair and the first sustainer electrode in the second sustainer electrode pair, the first sustainer electrode pad adapted to connected to a first sustainer voltage waveform supply whereby a single supply provides a first sustainer voltage waveform to both of the first sustainer electrodes. The display also includes a thin dielectric film covering the sustaining and auxiliary electrodes and a bottom glass substrate separated from the top glass substrate. The bottom substrate includes a plurality of alternating barrier ribs and microgrooves. An address electrode is associated with each microgroove and a phosphor is deposited over a portion of each address electrode. A first voltage is applied to the trigger electrode to initiate a discharge between the trigger electrode (84) and a sustaining electrode. A second voltage, that is greater than the first voltage is applied to the sustaining electrodes and causes the discharge to extend between the sustaining electrodes.

IPC 8 full level

G09G 3/20 (2006.01); **H01J 11/12** (2012.01); **H01J 11/28** (2012.01); G09G 3/292 (2013.01); G09G 3/298 (2013.01)

CPC (source: EP KR)

H01J 11/12 (2013.01 - EP); **H01J 11/22** (2013.01 - KR); **H01J 11/24** (2013.01 - KR); **H01J 11/26** (2013.01 - KR); **H01J 11/28** (2013.01 - EP KR); G09G 3/2927 (2013.01 - EP); G09G 3/2983 (2013.01 - EP); G09G 2310/0218 (2013.01 - EP); H01J 2211/46 (2013.01 - EP)

Citation (search report)

- [XY] WO 9844532 A1 19981008 - ORION ELECTRIC CO LTD [KR], et al
- [XY] US 6031329 A 20000229 - NAGANO SHINICHIRO [JP]
- [XY] WO 9909579 A1 19990225 - MATSUSHITA ELECTRIC IND CO LTD [JP], et al
- [A] WO 9826403 A1 19980618 - ORION ELECTRIC CO LTD [KR], et al
- [A] WO 9719438 A1 19970529 - ORION ELECTRIC CO LTD [KR], et al & EP 1024516 A1 20000802 - MATSUSHITA ELECTRONICS CORP [JP]

Designated contracting state (EPC)

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE TR

Designated extension state (EPC)

AL LT LV MK RO SI

DOCDB simple family (publication)

EP 1178512 A2 20020206; **EP 1178512 A3 20071121**; CN 1269094 C 20060809; CN 1345018 A 20020417; JP 2002100294 A 20020405; KR 20020011121 A 20020207; MX PA01007699 A 20040811

DOCDB simple family (application)

EP 01118278 A 20010730; CN 01123781 A 20010731; JP 2001232538 A 20010731; KR 20010046271 A 20010731; MX PA01007699 A 20010730