

Title (en)
METHOD AND APPARATUS FOR EFFICIENT VIDEO PROCESSING

Title (de)
VERFAHREN UND VORRICHTUNG ZUR EFFIZIENTEN VIDEOBEARBEITUNG

Title (fr)
PROCEDE ET APPAREIL PERMETTANT UN TRAITEMENT VIDEO EFFICACE

Publication
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Application
EP 00926096 A 20000417

Priority

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Abstract (en)
[origin: WO0064148A1] A video compression method and apparatus is disclosed. The present invention includes a "smart" or active decoder (Fig. 3) that performs much of the transmission and the instruction burden that would otherwise be required of the encoder, thus greatly reducing the overhead and resulting in a much smaller encoded bitstream. Thus, the corresponding (i.e., compatible) encoder of the present invention can produce an encoded bitstream with a greatly reduced overhead. This is achieved by encoding a reference frame (Fig. 3, element 7) based on the structural information inherent to the image (e.g., image segmentation, geometry, color, and/or brightness), and then predicting other frames relative to the structural information. Typically, the description of a predicted frame would include kinetic information (Fig. 3, element 6) (e.g., segment motion data and/or inexact matches and appearance of new information, and portion of the segment evolution that is captured by motion per se etc.). Because the decoder is capable of independently determining the structural information (and relationships thereamong) underlying the predicted frame, such information need not be explicitly transmitted to the decoder. Rather, the encoder need only send information that the encoder knows the decoder cannot determine on its own.

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Citation (search report)

- [X] EP 0720373 A1 19960703 - DAEWOO ELECTRONICS CO LTD [KR]
- [X] EP 0625853 A2 19941123 - NIPPON TELEGRAPH & TELEPHONE [JP]
- [A] EP 0783820 A2 19970716 - PHILIPS ELECTRONICS NV [NL], et al
- [A] US 5812787 A 19980922 - ASTLE BRIAN [US]
- [A] HARIDASAN R ET AL: "Scalable coding of video objects", CIRCUITS AND SYSTEMS, 1998. ISCAS '98. PROCEEDINGS OF THE 1998 IEEE INTERNATIONAL SYMPOSIUM ON MONTEREY, CA, USA 31 MAY-3 JUNE 1998, NEW YORK, NY, USA, IEEE, US, vol. 4, 31 May 1998 (1998-05-31), pages 289 - 292, XP010289442, ISBN: 978-0-7803-4455-6
- See references of WO 0064167A1

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