

Title (en)  
CIRCUIT SIMULATOR

Title (de)  
SCHALTUNGSSIMULATOR

Title (fr)  
SIMULATEUR DE CIRCUIT

Publication  
**EP 1181654 A1 20020227 (EN)**

Application  
**EP 00923175 A 20000407**

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Abstract (en)  
[origin: WO062208A1] An electronic circuit simulator technique dramatically reduces barriers to simulation of complex analog circuits. The invention reduces simulation preparation time by permitting as killed user to formulate a simulation strategy that can combine a modeling strategy, a deck extraction strategy, and a comprehensive suite of pre-engineered simulation tests that fully characterize a system under test. This pre-engineered simulation strategy can be applied to a new system very quickly in an automated fashion by a circuit designer having much less simulation experience than that of the skilled user who formulated the simulation strategy. The invention reduces the CPU time required for each test by allowing a skilled user to encode a device modeling strategy and a deck extraction strategy that effectively minimize the CPU time required to achieve a given simulation objective. For a given simulation objective, as killed user chooses for each element implemented in a simulation a device model of minimum complexity that achieves a desired level of simulation accuracy and extracts a portion of the complete production schematic that achieves the desired level of simulation accuracy. The invention reduces dramatically the effort required of a user to compose a required number of inputs to run a specified number of simulation tests and reduces dramatically the amount of CPU time required to run them.

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**G06F 17/50**

IPC 8 full level  
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CPC (source: EP)  
**G06F 11/261** (2013.01); **G06F 30/367** (2020.01)

Citation (search report)  
See references of WO 0062208A1

Citation (examination)  
• AU 4334200 A 20001114 - TRANSIM TECHNOLOGY CORP  
• WO 0062208 A1 20001019 - TRANSIM TECHNOLOGY CORP [US], et al

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