

Title (en)

APPARATUS AND METHOD FOR A PROGRAMMABLE SECURITY PROCESSOR

Title (de)

VORRICHTUNG UND VERFAHREN FÜR EINEN PROGRAMMIERBAREN SICHERHEITS-PROZESSOR

Title (fr)

APPAREIL ET PROCEDE POUR UN PROCESSEUR DE SECURITE PROGRAMMABLE

Publication

EP 1183603 A1 20020306 (EN)

Application

EP 00932153 A 20000505

Priority

- US 0012479 W 20000505
- US 13313199 P 19990507

Abstract (en)

[origin: WO0068791A1] A digital logic circuit having a programmable security circuit which stores a set of authorized configuration security keys (538). The security circuit compares authorization configuration keys (360) with incoming configuration request, and selectively enables a new configuration (544) for the programmable logic device.

IPC 1-7

G06F 11/00; **G06F 12/02**; **G06F 11/30**

IPC 8 full level

G06F 12/14 (2006.01); **G06F 21/76** (2013.01); **H04L 9/08** (2006.01)

CPC (source: EP KR)

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