

Title (en)  
CIRCUIT SUITABLE FOR VERTICAL INTEGRATION AND METHOD OF PRODUCING SAME

Title (de)  
VERTIKAL INTEGRIERBARE SCHALTUNG UND VERFAHREN ZU IHRER HERSTELLUNG

Title (fr)  
CIRCUIT POUVANT ETRE INTEGRE VERTICALEMENT ET SON PROCEDE DE PRODUCTION

Publication  
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Application  
**EP 00925226 A 20000419**

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Abstract (en)  
[origin: DE19918671A1] The invention relates to a circuit suitable for vertical integration and a method of producing same. In contrast to known methods for producing vertical electrical connections, the present method comprises process steps which are also used for the production of the vertically integratable circuit itself and which permit said vertical integration. This simplifies the overall production sequence of vertically integratable circuits and thus of the three-dimensionally integrated circuit as a whole, which in turn optimizes plant run-times since some process steps fall away. Furthermore, since the production of vertical electrical connections is no longer based on the use of finished substrates yields are improved, as process steps which could affect active circuit components already produced, such as steps involving high process temperatures, no longer have to be carried out after the production of said circuit components.

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IPC 8 full level  
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