

Title (en)

MINIMALLY-PATTERNED SEMICONDUCTOR DEVICES FOR DISPLAY APPLICATIONS

Title (de)

MINIMAL STRUKTURIERTES HALBLEITERBAUELEMENT FÜR ANZEIGEANWENDUNGEN

Title (fr)

DISPOSITIFS SEMI-CONDUCTEURS A STRUCTURE MINIMALE POUR APPLICATIONS D'AFFICHAGE

Publication

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Application

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Abstract (en)

[origin: WO0067327A1] A thin-film transistor array comprises at least first and second transistors. Each transistor comprises a source electrode, a drain electrode, a semiconductor electrode, a gate electrode, and a semiconductor layer. The semiconductor layer is continuous between the first and second transistors. The semiconductor layer is preferably unpatterned. In various display applications, the geometry of the transistors is selected to provide acceptable leakage currents. In a preferred embodiment, the transistor array is employed in an encapsulated electrophoretic display.

IPC 1-7

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IPC 8 full level

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