Title (en)
LOOP DRIVER FOR POTS, XDSL, OR INTEGRATED POTS/XDSL INTERFACE

Title (de)
LEITUNGSTREIBER FÜR POTS, XDSL, ODER POTS/XDSL INTEGRIERTE SCHNITTSTELLE

Title (fr)
CIRCUIT DE COMMANDE DE LIGNE POUR POTS, XDSL OU INTERFACE INTEGREE POTS/XDSL

Publication
EP 1186149 A1 20020313 (EN)

Application
EP 00942738 A 20000609

Priority
- CA 2274171 A 19990610
- US 0015992 W 20000609

Abstract (en)
[origin: WO0078013A1] A driver circuit (180) comprising a transformer having a primary winding (106, 108) for coupling to a subscriber loop and a secondary winding (104) for coupling to an interface circuit, a feedback circuit for coupling said primary winding to said secondary winding to provide a predetermined impedance match between said interface circuit and said loop over a predetermined frequency band of said driver circuit. In a preferred embodiment, the interface circuit is integrated line card for supporting both POTS and ADSL.

IPC 1-7
H04M 1/00

IPC 8 full level
H03F 1/56 (2006.01); **H04L 27/00** (2006.01); **H04M 11/00** (2006.01); **H04M 11/06** (2006.01); **H04Q 3/42** (2006.01); H04L 27/26 (2006.01)

CPC (source: EP)
H03F 1/565 (2013.01); **H04L 25/0266** (2013.01); **H04L 27/0002** (2013.01); **H04M 11/062** (2013.01); H04L 27/2601 (2013.01)

Designated contracting state (EPC)
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE

DOCDB simple family (publication)
WO 0078013 A1 20001221; AU 5732100 A 20010102; CA 2274171 A1 20001210; EP 1186149 A1 20020313; EP 1186149 A4 20040915; JP 2003502915 A 20030121

DOCDB simple family (application)
US 0015992 W 20000609; AU 5732100 A 20000609; CA 2274171 A 19990610; EP 00942738 A 20000609; JP 2001504145 A 20000609